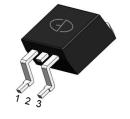




# N-Channel 40V (D-S) Power MOSFET

### **Features**

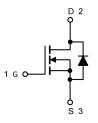
- 100% Avalanche Tested
- Extremely Low Losses with Low FOM Rdson\*Qg
- Halogen Free, Pb-Free
- RoHS Compliant



TO-263AB (D<sup>2</sup>PAK)

## **Applications**

- DC/DC
- Motors, lamps
- Power switching



Absolute Maximum Ratings (T <sub>J</sub> =25°C unless otherwise noted)						
Parameter		Symbol	Value	Unit		
Drain Source Voltage		V <sub>DS</sub>	40	V		
Gate Source Voltage		V <sub>GS</sub>	±20	V		
Drain Current, Continuous V <sub>GS</sub> =10V( <i>Note 1</i> )	T <sub>C</sub> =25°C I <sub>D</sub>		170	А		
Drain Current, Pulsed (Note 2)		I <sub>DM</sub>	680	Α		
Single Avalanche Energy @ L=0.5mH		E <sub>AS</sub>	719	mJ		
Power Dissipation(Note 3)	T <sub>C</sub> =25°C	P <sub>D</sub>	208	W		
Operating Junction/ Storage Temperature Range		TJ/ T <sub>STG</sub>	-55 to +150	°C		

Note 1: Calculated continuous current based on maximum allowable junction temperature.

Note 2: Repetitive rating; pulse width limited by max. junction temperature.

Thermal Characteristics					
Parameter	Symbol	Max	Unit		
Thermal Resistance Junction to Case(Note 3)	R <sub>thJC</sub>	0.72	°C/W		

Note 3: The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.



Electrical Characteristics (T <sub>J</sub> =25°C unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Drain Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40			V
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V			1	uA
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250uA	2		4	V
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA
Drain-Source On-state Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =40A		2.1	2.4	mΩ
Total Gate Charge	Qg	I <sub>D</sub> = 20A, V <sub>DS</sub> =20V, V <sub>GS</sub> = 10V		138		
Gate Source Charge	Q <sub>gs</sub>			25		nC
Gate Drain Charge	$Q_{gd}$			36		
Turn-on Delay Time	t <sub>d(on)</sub>	$V_{GS}$ =10V, $V_{DS}$ =20V, $R_{GEN}$ =3.6 $\Omega$ , $R_{L}$ =1 $\Omega$		32		
Turn-on Rise Time	t <sub>r</sub>			33		
Turn-off Delay Time	$t_{d(off)}$			70		ns
Turn-off Fall Time	t <sub>f</sub>			24		
Input Capacitance	C <sub>iss</sub>	V <sub>GS=</sub> 0V, V <sub>DS</sub> =40V, f=1MHz		10973		
Output Capacitance	Coss			650		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			536		

Reverse Diode Characteristics (T <sub>J</sub> =25°C unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Continuous Source Current (Body Diode)	Is	T <sub>C</sub> =25°C			170	A
Pulsed Source Current (Body Diode)	I <sub>SM</sub>			-	680	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =40A, V <sub>GS</sub> =0V			1.2	V
Reverse Recovery Time	Trr	I <sub>F</sub> =20A, di/dt = 100 A/μs		50	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	75	-	nC





# **Typical Characteristics Curves** (T<sub>J</sub> = 25°C unless otherwise noted)

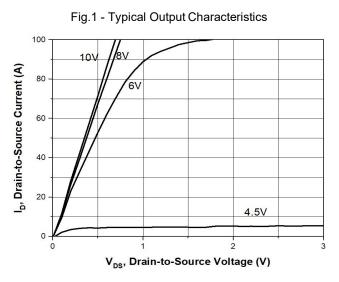


Fig.3 - RDS(on) vs. Junction Temperature

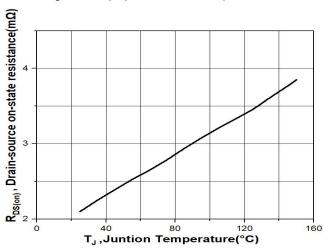


Fig.5 - Capacitance

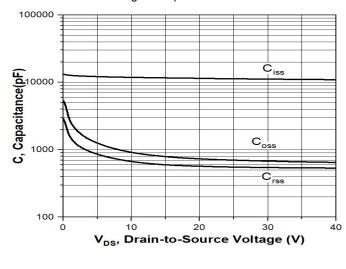


Fig.2 - Drain-to-Source Breakdown Voltage vs. Junction Temperature

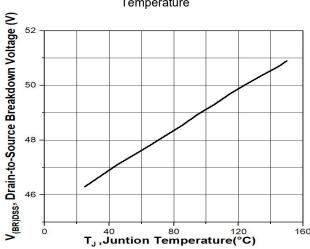


Fig.4 - Vth vs. Junction Temperature

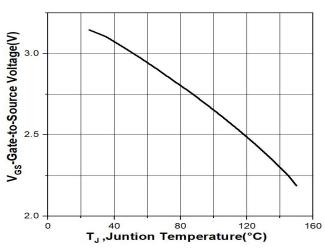
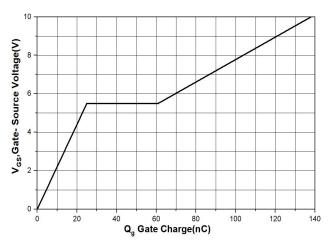


Fig.6 - Gate Charge





## **Typical Characteristics Curves** (T<sub>J</sub> = 25°C unless otherwise noted)

Fig.7 - Transfer Characteristics

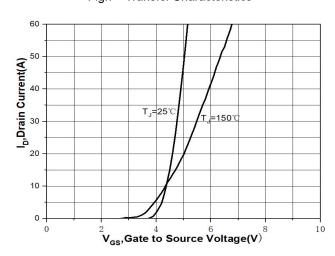
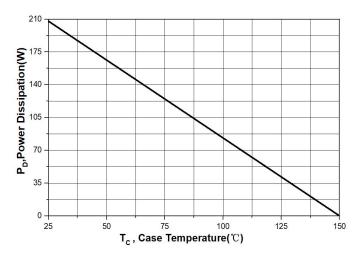


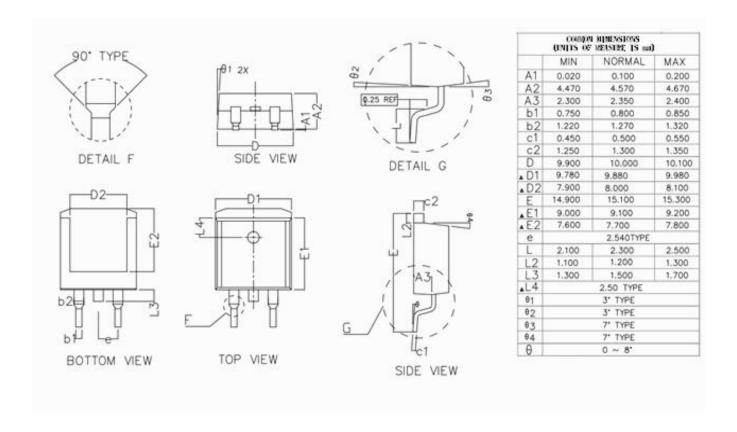
Fig.8 - Power Dissipation





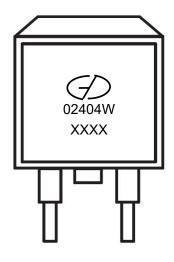
# Package Outline Dimensions (Unit: millimeters)

## **TO-263**





# **Marking Outline**



Part Name: GMN02404W

1. Logo Mark:

2. P/N Mark: 02404W

3. Date Code: XXXX



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