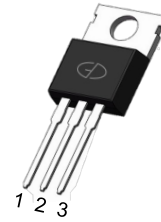


## 900mΩ,800V (D-S) Super Junction Power MOSFET

### Features

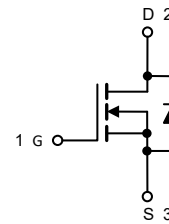
- 100% Avalanche Tested
- Extremely Low Losses with Low FOM  $R_{ds(on)} \cdot Q_g$
- Halogen Free, Pb-Free
- RoHS Compliant



TO-220AB

### Applications

- Solar inverter
- Telecom/Sever
- AC/DC power supply



### Absolute Maximum Ratings ( $T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain Source Voltage	$V_{DS}$	800	V
Gate Source Voltage	$V_{GS}$	$\pm 30$	V
Drain Current, Continuous $V_{GS}=10\text{V}$	$I_D$	$T_C=25^\circ\text{C}$	5
		$T_C=100^\circ\text{C}$	3.5
Drain Current, Pulsed (Note 1)	$I_{DM}$	12	A
Single Avalanche Energy (Note 2)	$E_{AS}$	225	mJ
Power Dissipation	$P_D$	$T_C=25^\circ\text{C}$	63
		$T_C=100^\circ\text{C}$	12
Operating Junction/ Storage Temperature Range	$T_J/ T_{STG}$	-55 to +150	$^\circ\text{C}$

Note 1: Single pulse;  $t_p \leq 1\mu\text{s}$ .

Note 2:  $V_{DD} = 50\text{V}$ ,  $I_D = 3\text{A}$ ,  $R_G = 25\Omega$ , starting  $T_J = 25^\circ\text{C}$ .

### Thermal Characteristics

Parameter	Symbol	Max	Unit
Thermal Resistance Junction to Case	$R_{thJC}$	2	$^\circ\text{C/W}$
Thermal Resistance Junction to Ambient (Note 3)	$R_{thJA}$	62	$^\circ\text{C/W}$

Note 3: Device mounted on 1 square inch FR4 PCB board, with 2oz single-sided copper, in a  $25^\circ\text{C}$  still air environment.

<b>Electrical Characteristics</b> ( $T_J = 25^\circ\text{C}$ unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	800	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=800V, V_{GS}=0V$	--	--	10	$\mu A$
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	2.5	--	4.5	V
Gate Leakage Current	$I_{GSS}$	$V_{GS}=\pm 30V, V_{DS}=0V$	--	--	$\pm 100$	nA
Drain-Source On-state Resistance (Note 4)	$R_{DS(on)}$	$V_{GS}=10V, I_D=12A$	--	800	900	m $\Omega$
Total Gate Charge	$Q_g$	$V_{GS}=0$ to 10V, $V_{DD}=400V, I_D=4A$	--	10.7	--	nC
Gate-Source Charge	$Q_{gs}$		--	2.7	--	
Gate-Drain Charge	$Q_{gd}$		--	4.97	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{GS}=10V, V_{DD}=400V, I_D=4A$	--	17.6	--	ns
Turn-on Rise Time	$t_r$		--	22.4	--	
Turn-off Delay Time	$t_{d(off)}$		--	64.2	--	
Turn-off Fall Time	$t_f$		--	28.2	--	
Gate Resistance	$R_g$	$V_{GS}=0V, f=1MHz$	--	37.4	--	$\Omega$
Input Capacitance	$C_{iss}$	$V_{GS}=0V, V_{DS}=100V, f=1MHz$	--	541	--	pF
Output Capacitance	$C_{oss}$		--	31.6	--	
Reverse Transfer Capacitance	$C_{rss}$		--	1.33	--	

<b>Reverse Diode Characteristics</b> ( $T_J = 25^\circ\text{C}$ unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Forward Current, Continuous	$I_{SD}$	$T_C=25^\circ\text{C}$	--	--	5	A
Diode Forward Voltage (Note 4)	$V_{SD}$	$I_F=5A, V_{GS}=0V$	--	0.86	--	V
Reverse Recovery Time	$T_{rr}$	$V_R=400V, I_F=1.1A, di/dt=100 A/\mu s$	--	181	--	ns
Reverse Recovery Charge	$Q_{rr}$		--	740	--	nC

Note 4: Pulse test; pulse width  $\leq 380\mu s$ , duty cycle  $\leq 1\%$ .

## Typical Characteristics Curves ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Fig.1 - Output Characteristics

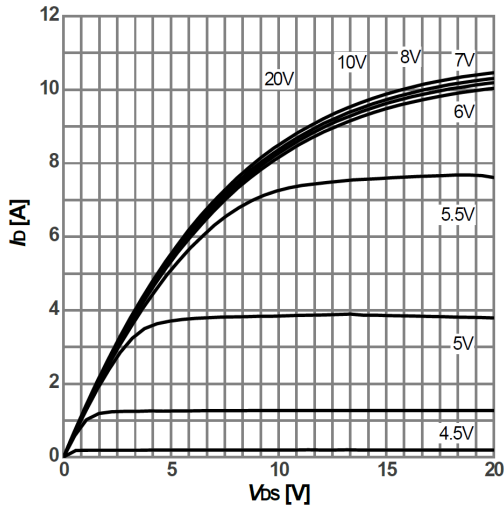


Fig.2 - Transfer Characteristics

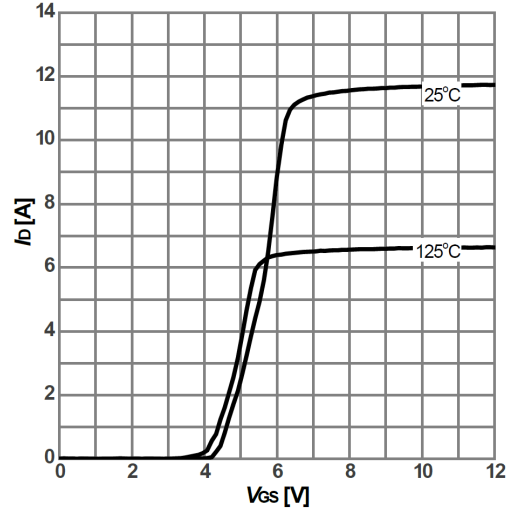


Fig.3 - Drain-Source On-Resistance

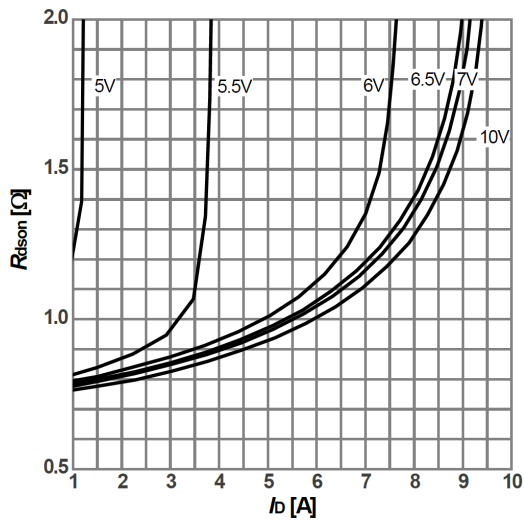


Fig.4 - Normalized On-Resistance

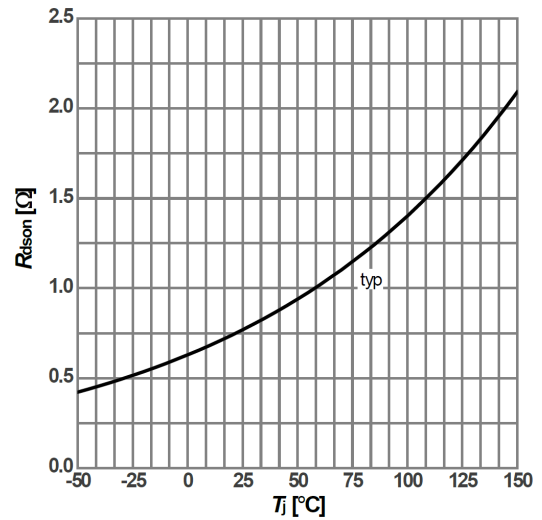


Fig.5 - Capacitance

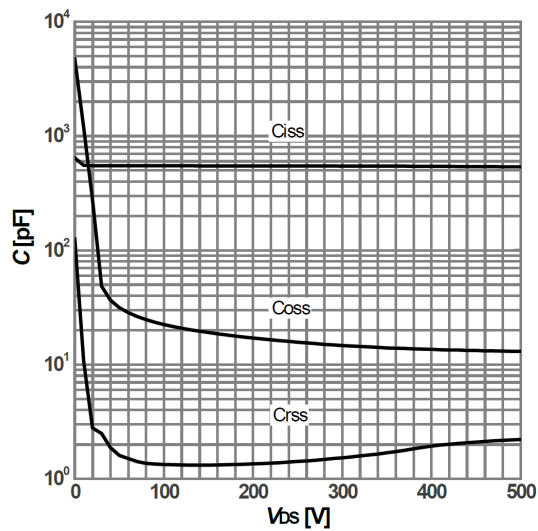
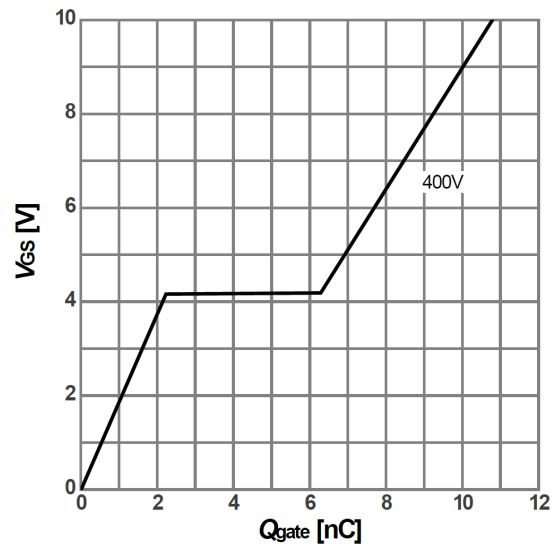


Fig.6 - Gate charge



## Typical Characteristics Curves ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Fig.7 - Forward Characteristic

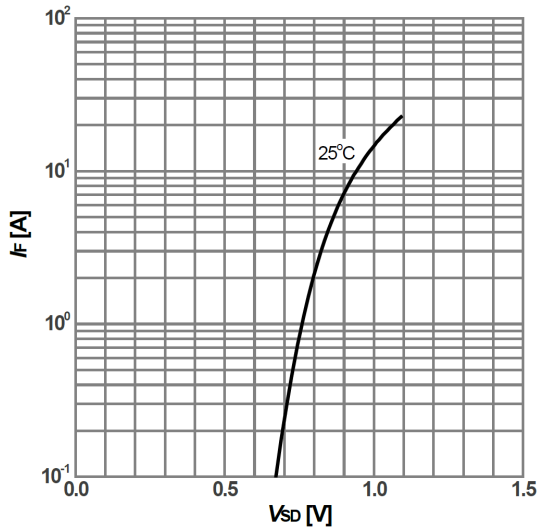


Fig.8 - Safe Operating Area

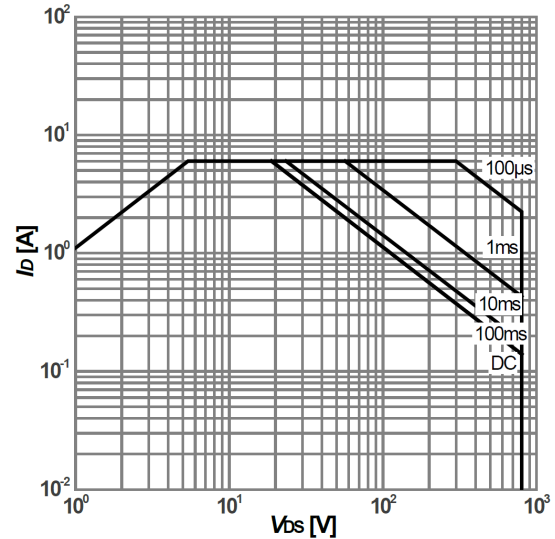


Fig.9 - Normalized Thermal Impedance, Junction-Case

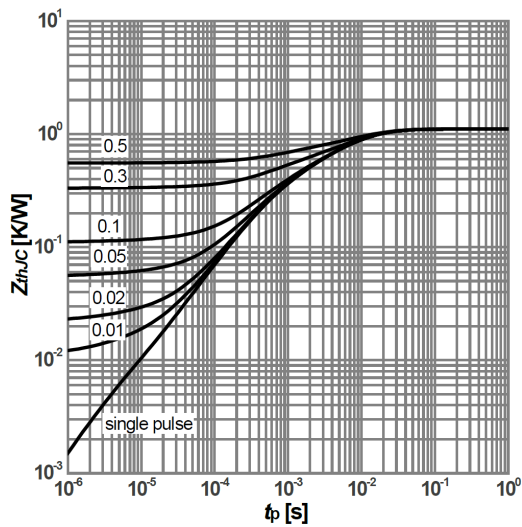


Fig.10 - Power Derating

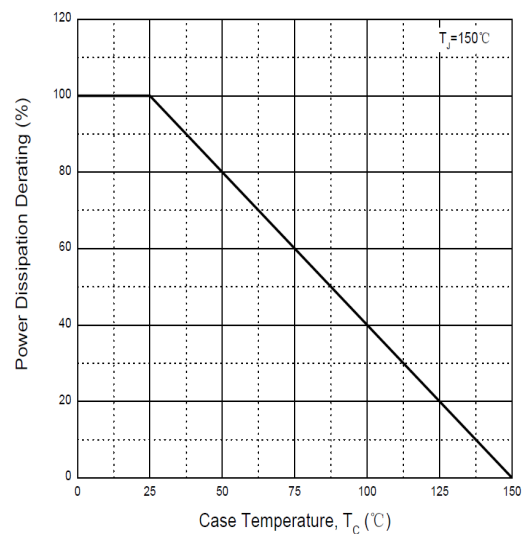
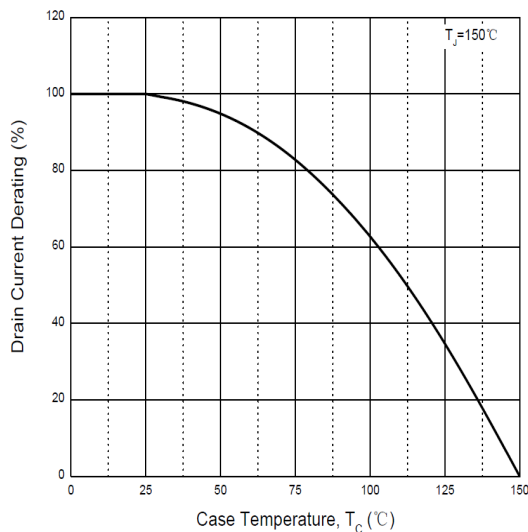
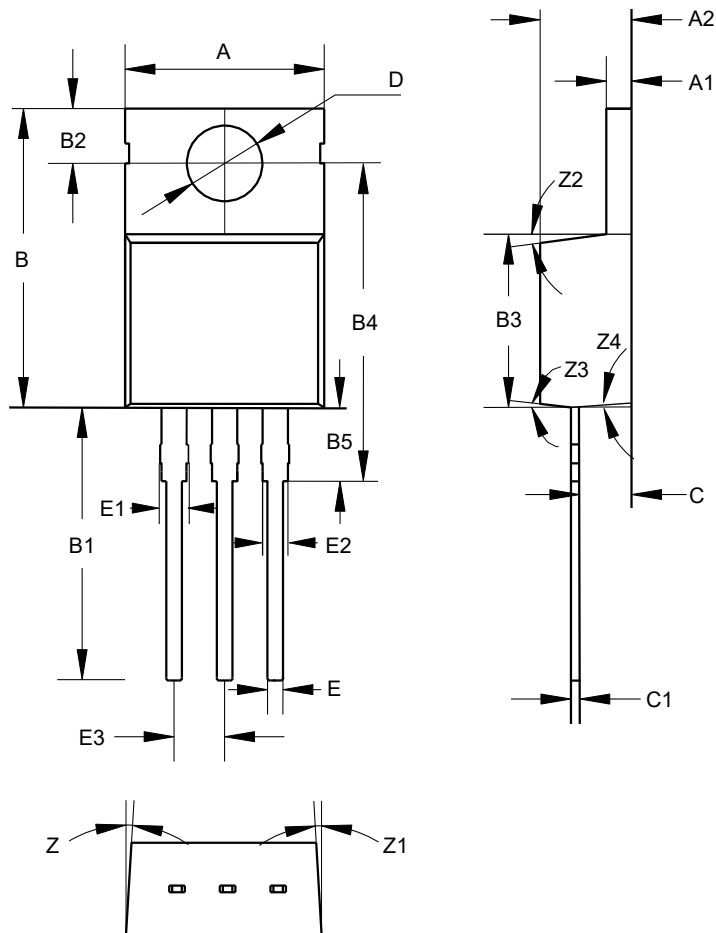


Fig.11 - Drain Current Derating



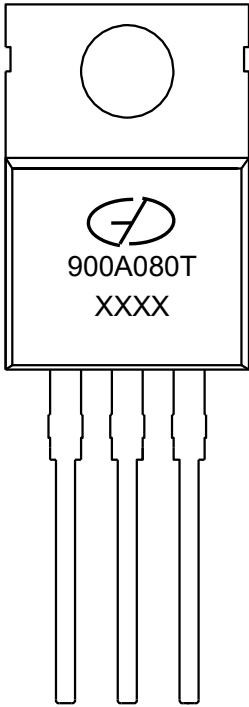
**Package Outline Dimensions** (Unit: millimeters)

**TO-220AB**




TO-220AB							
	Min.	Nom.	Max.		Min.	Nom.	Max.
A	9.8	10	10.2	D	3.7	3.8	3.9
A1	1.17	1.27	1.37	E	0.68	0.78	0.88
A2	4.5	4.6	4.7	E1	1.2	1.4	1.6
B	14.5	15	15.5	E2	1.17	1.27	1.37
B1	13.2	13.7	14.2	E3	2.44	2.54	2.64
B2	2.65	2.75	2.85	Z	-	3°	-
B3	8.5	8.7	8.9	Z1	-	3°	-
B4	15.5	16	16.5	Z2	-	7°	-
B5	3.4	3.7	4.0	Z3	-	7°	-
C	2.3	2.6	2.9	Z4	-	1.5°	-
C1	0.28	0.38	0.48	-	-	-	-

## Marking Outline



Part Name: GMN900A080T

1. Logo Mark: 
2. P/N Mark: 900A080T
3. Date Code: XXXX

## Revision History

Version	Date	Major Changes
Rev.A	2024.08.29	Official Release

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