

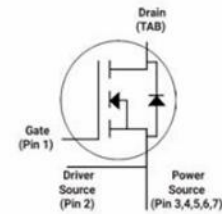
N-Channel 1200V (D-S) SiC MOSFET

Features

- Wide bandgap SiC MOSFET technology
- Low On-Resistance with High Blocking Voltage
- Low Capacitances with High-Speed switching
- Low reverse recovery(Qrr)
- Halogen free, RoHs compliant



TO-263-7



Applications

- Switch mode power supplies
- Renewable energy
- On Board Charger
- High voltage DC/DC converters

Absolute Maximum Ratings (T_J=25°C unless otherwise noted)

Parameter	Symbol	Ratings	Unit
Drain Source Voltage	V _{DS}	1200	V
Gate Source Voltage	V _{GS}	-8/+22	V
Recommend Gate Source Voltage	V _{GSop}	-4/+18	V
Drain Current Continuous V _{GS} =18V	I _D	T _C =25°C	30
		T _C =100°C	21
Power Dissipation	P _D	136	W
Operating Temperature and Storage Temperature Range	T _J /T _{STG}	-55 to +175	°C

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance Junction to Case	R _{thJC}	1.1	°C/W
Thermal Resistance Junction to Ambient	R _{thJA}	40	°C/W

Electrical Characteristics (T _J =25°C unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =100μA	1200	--	--	V
Gate Leakage Current	I _{GSS}	V _{GS} =18V, V _{DS} =0V	--	--	100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =1200V, V _{GS} =0V	--	1	5	μA
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =5mA	2	2.8	4	V
		V _{DS} =V _{GS} , I _{DS} =5mA, T _J =175°C	--	1.9	--	
Drain-Source On-state Resistance	R _{Ds(on)}	V _{GS} =18V, I _D =20A	--	75	85	mΩ
		V _{GS} =18V, I _D =20A, T _J =175°C	--	125	--	
Drain-Source On-state Resistance	R _{Ds(on)}	V _{GS} =15V, I _D =20A	--	90	110	mΩ
		V _{GS} =15V, I _D =20A, T _J =175°C	--	140	--	
Total Gate Charge	Q _g	V _{GS(off)} =-4V, V _{GS(on)} =18V V _{DD} =800V, I _D =20A	--	40	--	nC
Gate Source Charge	Q _{gs}		--	7	--	
Gate Drain Charge	Q _{gd}		--	19	--	
Turn-On Switching Energy	E _{ON}	V _{GS} =-4/+18V, V _{DD} =800V · I _D =20A, R _G =0Ω, L=100uH	--	320	--	μJ
Turn-Off Switching Energy	E _{OFF}		--	49	--	
Turn-on Delay Time	t _{d(on)}		--	19	--	ns
Turn-on Rise Time	t _r		--	21	--	
Turn-off Delay Time	t _{d(off)}		--	15	--	
Turn-off Fall Time	t _f	--	17	--		
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =1000V, V _{AC} =25mV, f=1MHz	--	920	--	pF
Output Capacitance	C _{oss}		--	57	--	
Reverse Transfer Capacitance	C _{rss}		--	3.9	--	
Gate resistance	R _G	V _{AC} = 25mV, f=1MHz	--	1.5	--	Ω

Reverse Diode Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Diode Forward Voltage	V_{SD}	$I_{SD}=10\text{A}, V_{GS}=-4\text{V}$	--	4.2	--	V
		$I_{SD}=10\text{A}, V_{GS}=-4\text{V}, T_J=175^\circ\text{C}$	--	3.8	--	
Continuous Diode Forward Current	I_{SD}	$V_{GS}=-4\text{V}$	--	80	--	A
Reverse Recovery Time	t_{rr}	$V_R=800\text{V}, I_F=20\text{A}, V_{GS}=-4\text{V}, di/dt=700\text{A}/\mu\text{s}$	--	41	--	ns
Reverse Recovery Charge	Q_{rr}		--	405	--	nC

Typical Characteristics Curves ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Figure 1. Output Characteristic ($T_J = -40^\circ\text{C}$)

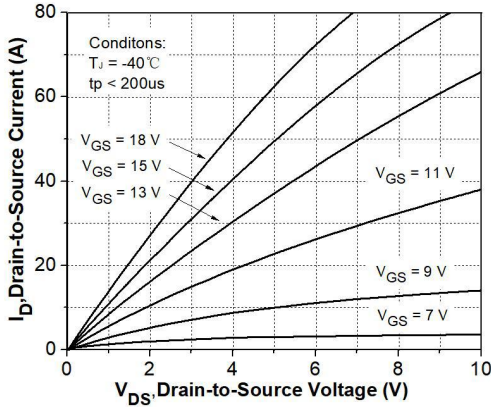


Figure 2. Output Characteristic ($T_J = 25^\circ\text{C}$)

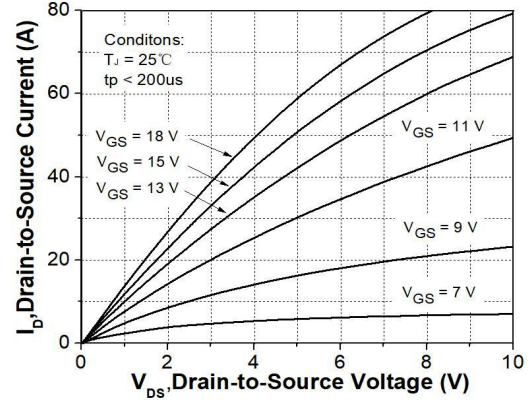


Figure 3. Output Characteristic ($T_J = 175^\circ\text{C}$)

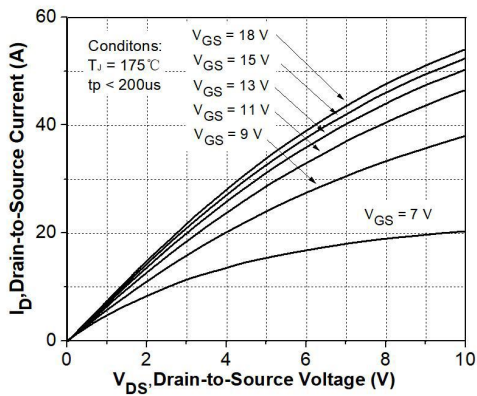


Figure 4. $R_{DS(on)}$ Vs I_{DS} Characteristic

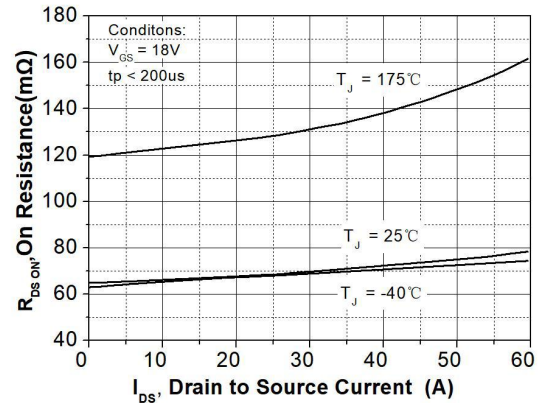


Figure 5. $R_{DS(on)}$ vs. Temperature

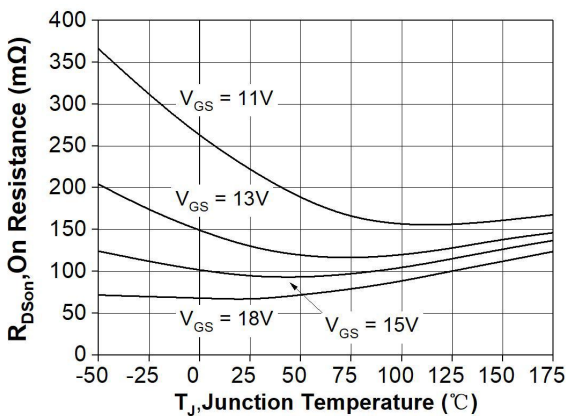
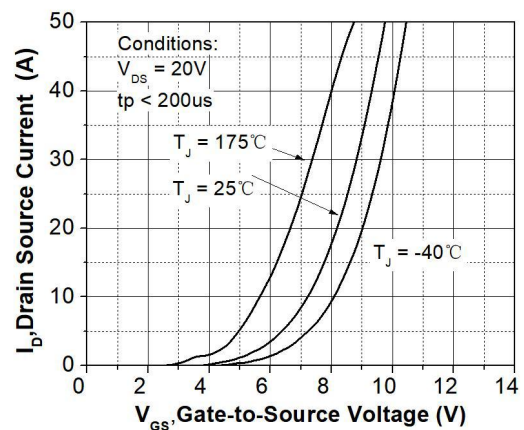


Figure 6. Transfer Characteristic



Typical Characteristics Curves ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Figure 7. Body-diode Characteristic ($T_J = -40^\circ\text{C}$)

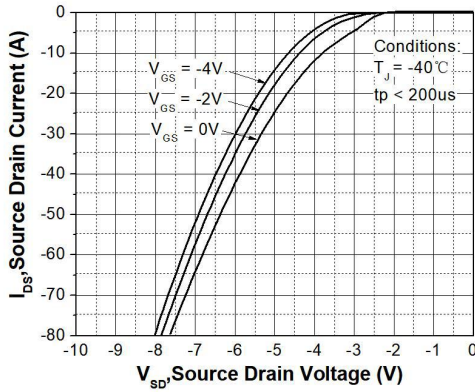


Figure 8. Body-diode Characteristic ($T_J = 25^\circ\text{C}$)

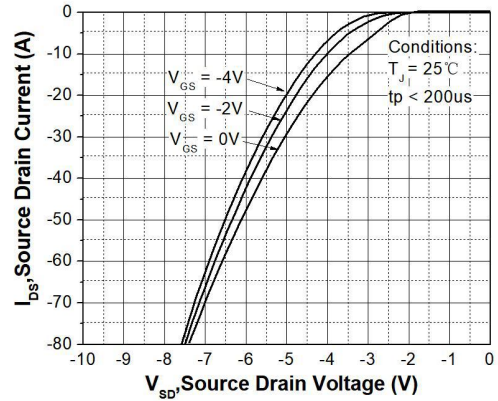


Figure 9. Body-diode Characteristic ($T_J = 175^\circ\text{C}$)

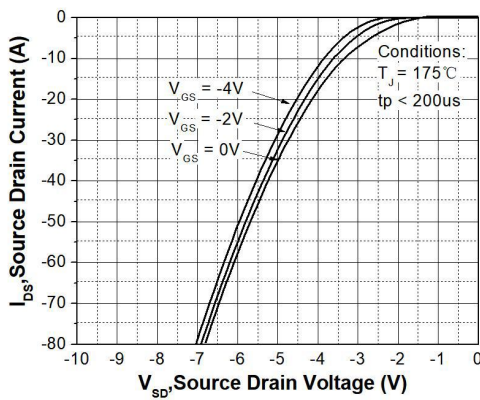


Figure 10. V_{TH} Vs T_J Temperature Characteristic

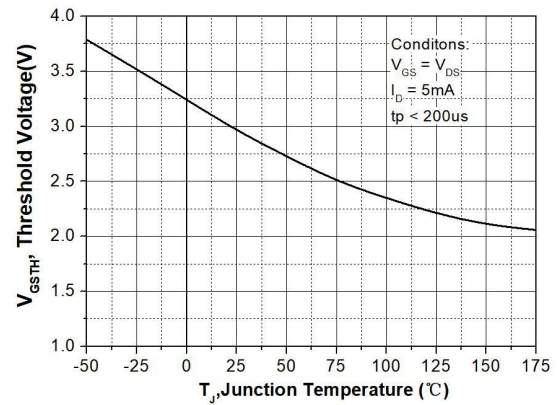


Figure 11. 3rd Quadrant Characteristic ($T_J = -40^\circ\text{C}$)

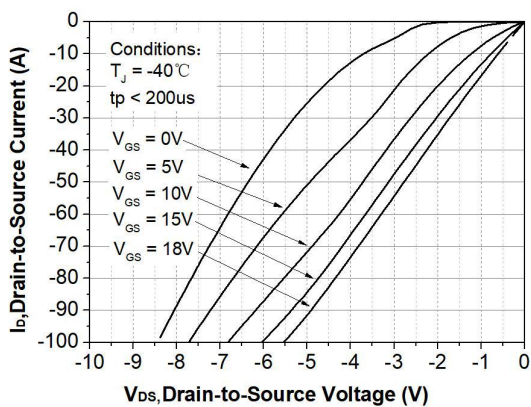
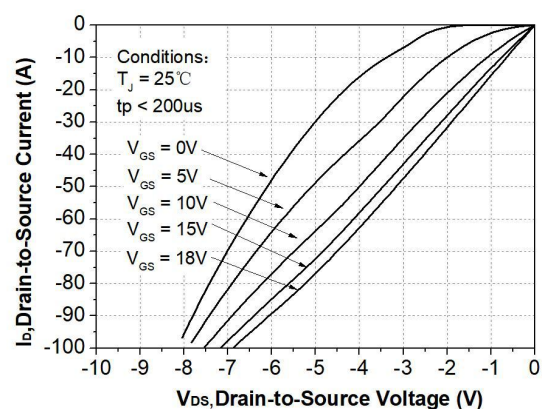


Figure 12. 3rd Quadrant Characteristic ($T_J = 25^\circ\text{C}$)



Typical Characteristics Curves ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Figure 13: 3rd Quadrant Characteristic($T_J=175^\circ\text{C}$)

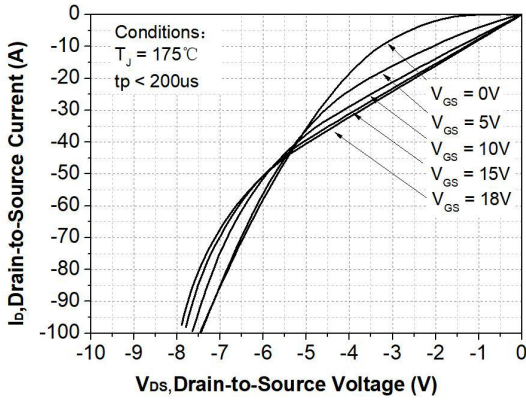


Figure 14: Gate Charge Characteristics

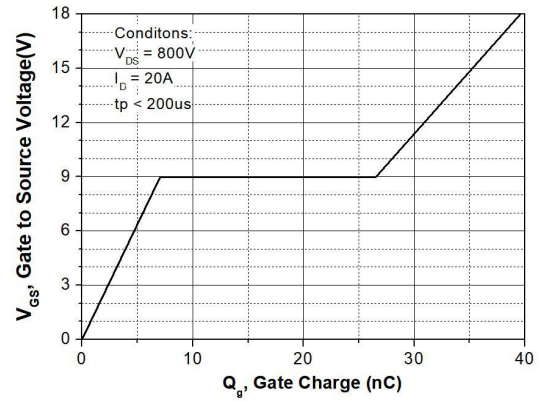


Figure 15: Safe Operating Area

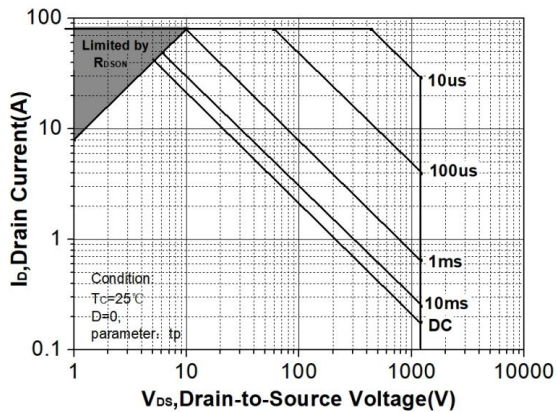


Figure 16: Capacitance Characteristics

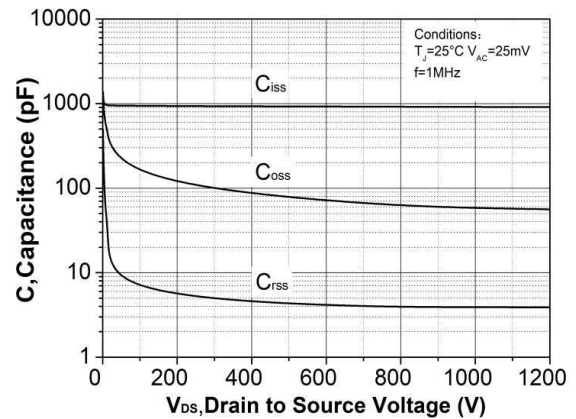
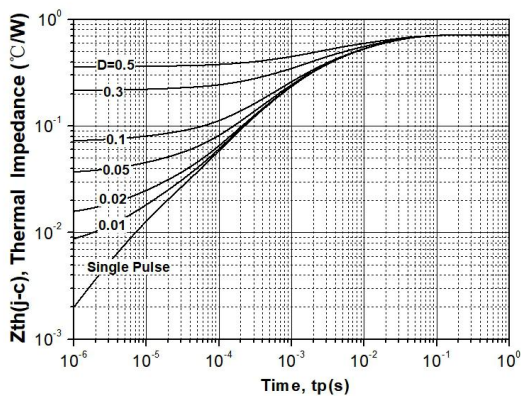


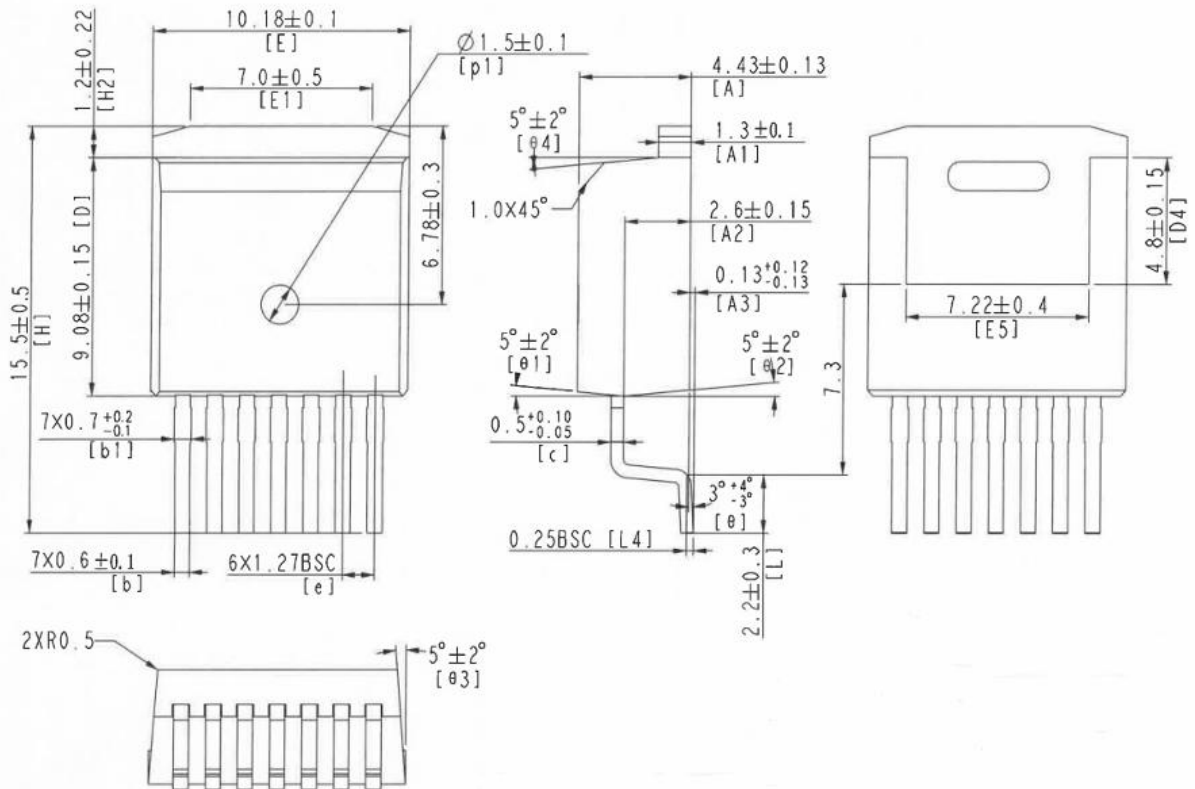
Figure 17: Transient Thermal Impedance



Package Outline Dimensions (Unit: millimeters)

TO-263-7

UNIT:mm



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