

30A, 650V Silicon Carbide Schottky Diode

Features

- High-Frequency Operation
- Zero Reverse Recovery Current
- Temperature-Independent Switching
- Extremely Fast Switching
- Plastic package has underwriters Laboratory Flammability Classification 94V-0
- Halogen-free according to IEC 61249-2-21

Applications

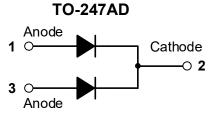
- Boost Diodes in PFC or DC/DC stages
- LED Lighting Power Supplies
- Power Factor Correction

Mechanical Data

- Case: Epoxy, Molded
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 sec
- Shipped 30 units per plastic tube

Maximum Ratings & Electrical Characteristics(TA=25°C unless otherwise noted)					
Parameter	Symbol	GS30D065CP	Unit		
Maximum repetitive peak reverse voltage		Vrrm 650		V	
Working peak reverse voltage	Vrwm 650		V		
Maximum DC blocking voltage	VDC	650	V		
	Tc=25°C		41/82		
Maximum average forward rectified current	Tc=135°C	lf(AV)	18/36	А	
	Tc=145°C		15/30		
Peak forward surge current, tp=10ms,Half Sine	IFSM	120*	А		
Dower dissinction	Tc=25°C	Ptot	136*	w	
Power dissipation	Tc=110°C	Ftot	59*		
Operating junction temperature range	TJ	-55 to +175	°C		
Storage temperature range	Тѕтс	-55 to +175	°C		





Electrical Specifications(TA=25°C unless otherwise noted)						
Parameter	Symbol	Test Conditions	Тур	Max	Unit	
Forward drap voltage		IF=15A, TJ=25°C	1.42	1.70	V	
Forward drop voltage	VF	IF=15A, TJ=175°C	1.70	2.50	v	
Reverse leakage current @rated VR	In	V _R =650V, TJ=25℃	3	100		
Reverse leakage current @rateu vk	lr	V _R =650V, TJ=175℃	20	250	μA	
Total capacitive charge	Qc	VR=400V, IF=10A, TJ=25°C	47	-	nC	
Total capacitance	С	VR=400V, TJ=25°C, f=1MHz	69	-	pF	

Thermal-Mechanical Specifications (TA=25°C unless otherwise noted)					
Parameter	Symbol	Тур	Max	Unit	
Thermal Resistance, Junction to Case	Rejc	1.1*	-	°C /W	

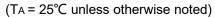
Note:

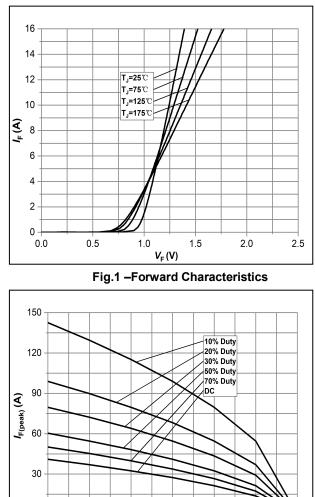
*Per Leg, **Per Device

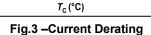


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Ratings and Characteristics Curves







100

125

150

175

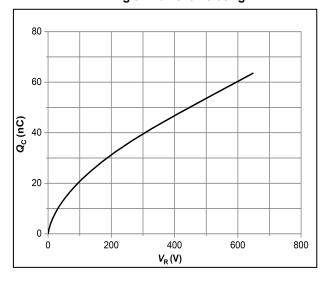


Fig.5 – Total Capacitance Charge vs. Reverse Voltage

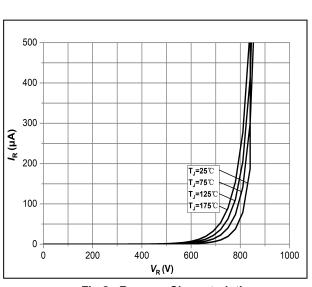


Fig.2 – Reverse Characteristics

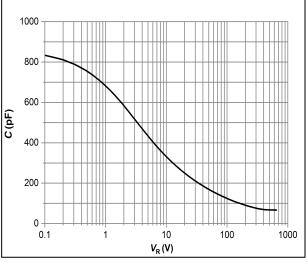
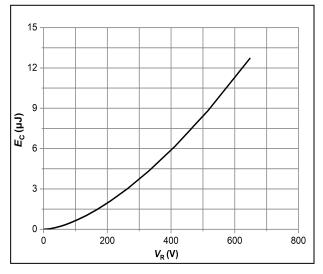


Fig.4 – Capacitance vs. Reverse Voltage





0 -

25

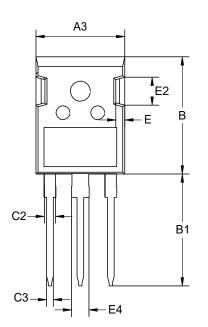
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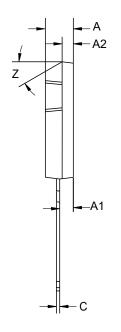
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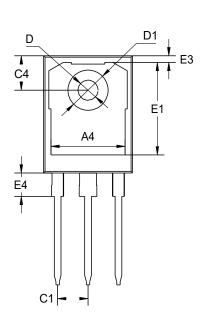


Package Outline Dimensions (Unit: millimeters)

TO-247AD



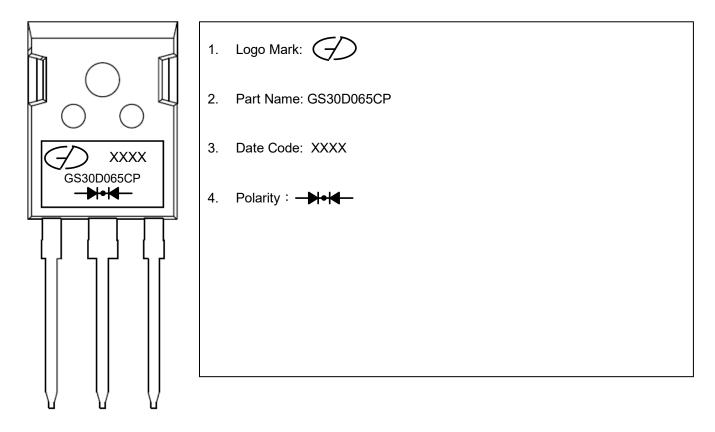




TO-247AD							
	Min.	Nom.	Max.		Min.	Nom.	Max.
А	4.7	5	5.2	C3	1.1	1.2	1.3
A1	2.3		2.5	C4	6.04	6.15	6.30
A2	1.9	2	2.1	D	3.5	3.6	3.7
A3	15.48	15.88	16.28	D1	7	7.19	7.4
A4	13.06	13.26	13.56	E	1.5	1.6	1.7
В	20.8	20.95	21.1	E1		16.55	
B1	19.8	20	20.32	E2	4.9	5.0	5.1
С	0.5	0.6	0.7	E3	0.95	1.17	1.35
C1	5.34	5.44	5.54	E4		4.17	4.5
C2		2		Ζ		30°	



Marking Outline



Revision History

Document Version	Date of release	Description of changes
Rev.A	2022.08.17	Preliminary Datasheet



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